Sr. No.	BCA SEM – II : COA : MCQ : Databank
1	NOT gate is also known as _____.
2	_____ are the building blocks of all the circuit in a computer.
3	_____ is a such type of memory that cannot be changed by program or user.
4	_____ is a fast temporary type of memory in which programs, applications and data are stored.
5	A tabular representation of all the possible combinations of the input variables and the corresponding output is called _____.
6	_____ gate is also called as an all or nothing gate.
7	The _____ gate operation is represented by a plus sign.
8	_____ gate is also called as any or all gate.
9	_____ gate is combination of AND gate and NOT gate.
10	_____ gate is also known as inequality detector.
11	_____ gate is combination of NOT gate and XOR gate.
12	_____ is an algebra that deals with binary variables and logic operations.
13	A circuit diagram by using logic gates, is used to show so the logical operations of given Boolean function is called _____.
14	_____ theorem is very useful in dealing with NOR and NAND gate.
15	The complement of the _____ of two Boolean variables is equal to the sum of the compliments of these two variables.
16	A _____ is a systematic method for simplifying Boolean expressions graphically.
17	The standard SUM terms at which the output is 0 or minimum is called _____ .
18	A _____ is a diagram made of squares which each square representing one minterm or maxterm.
19	The number of square in in map of _____ variable is 2^n.
20	_____ variable k-map will have 16 cells or squares.

21	The connected arrangement of the logic gates with a set of inputs and outputs is known as _____ circuits.
22	A _____ circuit is an interconnection of logic gates and memory elements.
23	The memory elements used in sequential circuits are called _____.
24	A combinational circuit that performs the arithmetic addition of two bit is called _____.
25	A flip flops without _____ is known as latches.
26	Don't care condition represented as _____ symbol.
27	A Logic state can be either _____ or 1.
28	Which of the following is not a universal gate?
29	To design K-map of F=ABC+AD, we required to draw _____ variable K-map.
30	Which one of the following is not a valid rule of boolean algebra ?
31	A flip-flop is a binary cell capable of storing information of _____.
32	Sum of Products function is also known as _____.
33	Which of the following can be developed with use of Flip-Flop?
34	How many states does a flip flop has ?
35	A group of 4 adjacent cell in K-map is known as _____.
36	A XOR B = _____.
37	A(B+C) = _____.
38	_____ = A AND B
39	_____ = (X+Y)(X+Z)
40	F(P,Q,R) = (Q+R')(P'+R') is an example of _____ function.